SHENZHEN TIBTRONIX TECHNOLOGY CO., LTD.



TFPLEG01D

25Gb/s SFP28 Transceiver Hot Pluggable, Duplex LC, +3.3V, 850nm 100m VCSEL, Multi mode

2015/6/1



Product Features

- Supports up to 25.78Gbps bit rates
- Hot-pluggable SFP+ footprint
- > 850nm VCSEL laser and PIN photodiode
- ➤ Up to 70m for OM3-MMF and 100m for OM4-MMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- ➤ Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:

Standard: 0 to +70°C

Applications

> 25.78Gb/s single lane 100GE SR4

Description

The SFP28 transceivers are high performance, cost effective modules supporting data rate of 25.78Gbps and 70m transmission distance with OM3 MMF or 100m transmission distance with OM4 MMF.

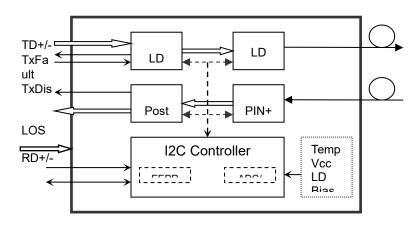
The transceiver consists of three sections: a VCSEL laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

Fiber type	Data rate (Gbps) Operating range (
	4.25	0.5~150
OM2	8.5	0.5~50
	14.025	0.5~35
	4.25	0.5~380
OM3	8.5	0.5~150
	14.025	0.5~100

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Transceiver functional diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C

Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			300	mA
Data Rate			25.78		Gbps

Optical and Electrical Characteristics

Parai	Parameter		Min	Typical	Max	Unit	Notes
	Transmitter						
Centre V	Vavelength	λς	840	850	860	nm	
Spectral W	/idth (RMS)	Δλ			0.6	nm	
Side-Mode Su	uppression Ratio	SMSR	-	-	-	dB	
Average C	Output Power	P _{out}	-8.4		2.4	dBm	1
Extinct	tion Ratio	ER	2.0			dB	
Data Input Sv	wing Differential	V _{IN}	180		950	mV	2
Input Differer	ntial Impedance	Z _{IN}	90	100	110	Ω	
TV Dis abla	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TV F It	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiv	er			
Centre V	Vavelength	λς	840	850	860	nm	
Receive	r Sensitivity				-10	dBm	3
Receive	r Overload		2.4			dBm	3
LOS D	LOS De-Assert				-13	dBm	
LOS	LOS Assert		-30			dBm	
LOS H	LOS Hysteresis		0.5		4	dB	
Data Output S	Swing Differential	V _{out}	500	700	900	mV	4
L	.OS	High	2.0		Vcc	V	

Low		0.8	V	
				1

Notes:

- The optical power is launched into MMF.
 PECL input, internally AC-coupled and terminated.
 Measured with a PRBS 2³¹-1 test pattern @25.78Gbps, BER ≤1×10⁻⁴.
 Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 20	mA	±10%	Internal

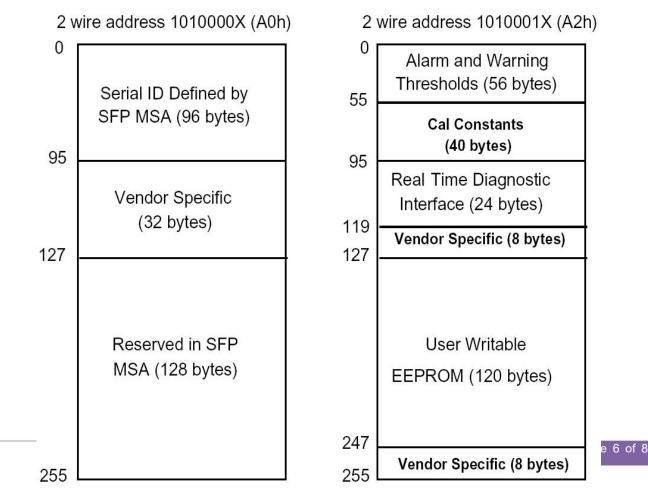
TX Power	-8.0 to 3	dBm	±3dB	Internal
RX Power	-14 to 0	dBm	±3dB	Internal

Digital Diagnostic Memory Map

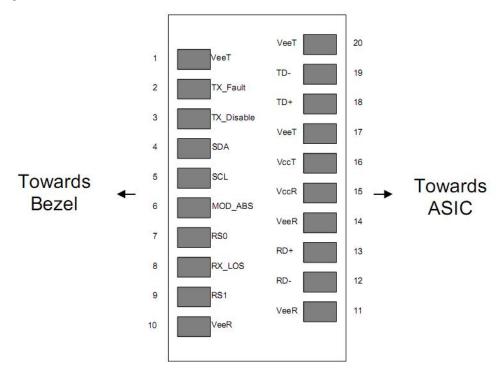
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Descriptions



Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3

TSPLSG01D

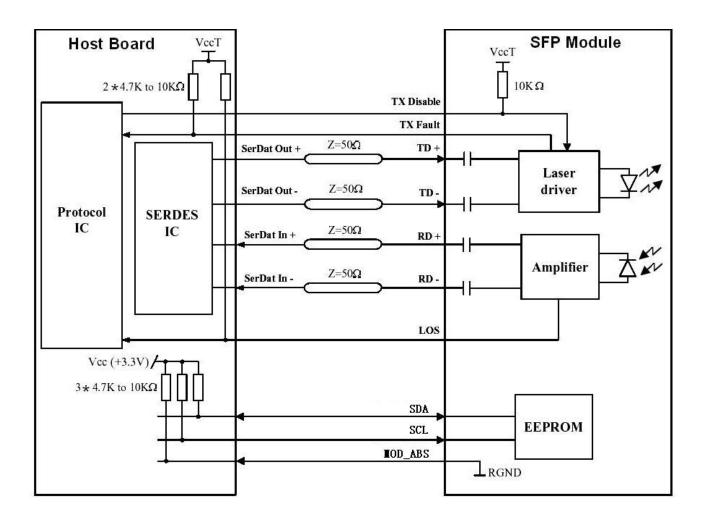
9	RS1	Not Connected	3	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V _{EET}	Transmitter Ground	1	

Notes

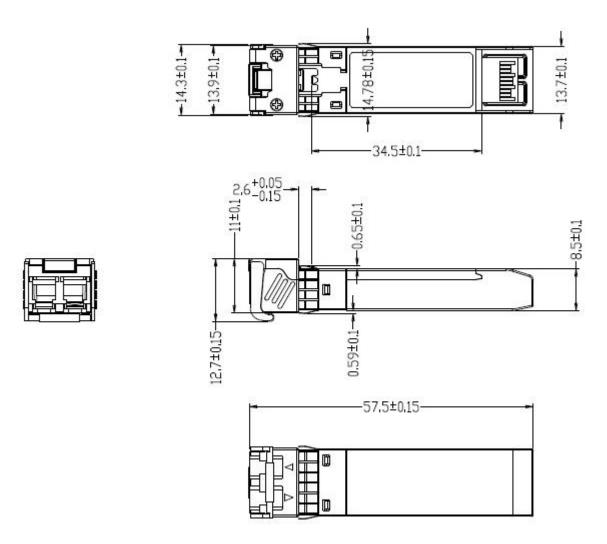
Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
TFPLEG01D	850nm, 25.78Gbps, LC, OM3-MMF 70m/OM4-MMF 100m, 0°C~+70°C

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